



## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**Complete if Known**

Application Number	09/632,522
Filing Date	August 4, 2000
First Named Inventor	Efstathios Papaefstathiou
Group Art Unit	2782
Examiner Name	Not Assigned
Attorney Docket Number	205101

Sheet	1	of	2
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Examiner Initials	Doc. No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number (s), publisher, city and/or country where published.	Translation	
			Yes	No**
Capel	AA	HARRISON, P.G., "Performance Modelling of Communication Networks and Computer Architectures", ( <i>Addison-Wesley Publishing Company</i> ) pp. 1-472 (1993)		
Capel	AB	BHUYAN, Laxmi, N. et al., "Performance of Multiprocessor Interconnection Networks", (February 1989) pp. 25-37		
Capel	AC	BAJAJ, Sandeep, et al., "Improving Simulation for Network Research", University of Southern California Science Department Technical Report 99-702B, March 1999 (revised September 1999), pp 1-11		
Capel	AD	NELSON, Randolph, et al., "Performance Analysis of Parallel Processing Systems", <i>IEEE Transactions on Software Engineering</i> , 14(4):532-(April 1988)		
Capel	AE	JAIN, Raj, "The Art of Computer Systems Performance Analysis" Techniques for Experimental Design, Measurement, Simulations and Modeling, ( <i>John Wiley &amp; Sons, Inc.</i> ) pp. 1-685, (1991)		

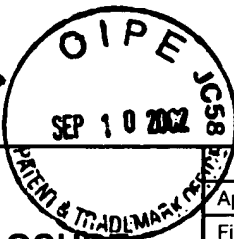
**Examiner Signature**

April Sharon

Date Considered

3/5/04

- \* A concise statement of relevance is being submitted in lieu of a translation. 37 CFR 1.98(a)(3).
- + An English-language equivalent/patent, or an English-language abstract, or an English-language version of the search report or action by a foreign patent office in a counterpart foreign application indicating the degree of relevance found by the foreign office is being submitted in lieu of a concise explanation of relevance under 37 CFR 1.98(a)(3).



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